

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1681	(711/103).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/17 16:53
L3	2277	(711/202,203).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/17 16:53
L5	3608	(714/718,719).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/17 16:53
L7	1053	(711/131,149,150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/17 16:53
L9	0	L1 AND L3 AND L5 AND L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:53
L11	0	L1 AND L3 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L13	3	L3 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L15	263010	"SoC" System-on-chip "system on chip" system-on-a-chip "system on a chip" "system on programmable chip" "system on a programmable chip"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54

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L17	3987	System-on-chip "system on chip" system-on-a-chip "system on a chip" "system on programmable chip" "system on a programmable chip"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L19	268214	"nonvolatile memory" ((nonvolatile non-volatile) ADJ memory) "flash memory" EPROM "electrically erasable programmable read only memory" flashchip flashdisc flashdisk flashcard flashram flashrom (flash ADJ (memory OR memories OR bios OR chip OR chips OR card OR cards OR disk OR disc OR ram OR rom OR storag\$5 OR module\$5)) (flash ADJ (random ADJ access ADJ memory OR read ADJ only ADJ memory)) (flash ADJ "electrically erasable programmable read only memory") "flash EPROM" FEEPROM "flash EEPROM" E2PROM EAROM EEPROM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L21	2	(NVSBI ADJ memory) "non-volatile semiconductor-based imperfect"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L23	3	(NVSBI) "non-volatile semiconductor-based imperfect"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L25	13	L5 AND L17 AND L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L27	659	L5 AND L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54

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L29	16	L5 AND L15 AND L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:54
L31	1	L27 AND L3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L33	10379	((block sector section partition) NEAR5 (address location)) WITH (translat\$3 conver\$4 transform\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L35	15	L27 AND L33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L37	391120	(block sector section partition) WITH (translat\$3 conver\$4 transform\$5 map)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L39	1870	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L41	3	L39 AND L5 AND L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L43	10	L39 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55

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L45	3	L27 AND L39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L47	431	((memory) NEAR2 ((error fault) NEAR2 detect\$3)) WITH (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L49	1987	((error fault defect) NEAR2 (detect\$3 determin\$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L51	1870	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L53	58	L49 AND L51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:55
L55	4564	"circular buffer"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L57	1987	((error fault defect) NEAR2 (detect\$3 determin\$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L59	1870	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57

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L61	58	L57 AND L59	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L63	1	L55 AND L61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L65	46	L55 AND L57	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L67	1987	((error fault defect) NEAR2 (detect\$3 determin\$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L69	1870	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L71	58	L67 AND L69	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57
L73	14	synchroniz\$5 AND L71	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 16:57